



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : YEH
Application No. : 10/642,822
Filed : August 19, 2003
Title : REDUCED MEMORY ARCHITECTURE
FOR EDGE MODULATOR
Group Art Unit : 2611
Examiner : A. Ettehadieh
Docket No. : BHT/3212-37

MAIL STOP AMENDMENT

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Office Action of January 16, 2007, please amend the above-identified application as follows:

Application No. 10/642,822

IN THE DRAWINGS:

Please delete Figures 1 and 2 as originally filed with this application. Enclosed is one (1) replacement sheet for Figures 1 and 2, accompanied by a Letter to the Official Draftsperson. Figures 1 and 2 have been amended to be labeled "Prior Art" as required by the Examiner.